



PATENT  
005702-20035

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

**KITAMURA, et al.**

Serial No: 09/392,865

Filed: September 9, 1999

For: **NON-VOLATILE SEMICONDUCTOR  
MEMORY DEVICE AND ITS  
MANUFACTURING METHOD**

Art Unit: 2813

Examiner: C. Bowers

6/ I.D.S.  
G. Stenly  
11-30-99

**TRANSMITTAL OF INFORMATION DISCLOSURE  
STATEMENT**

Box AMENDMENT  
Assistant Commissioner for Patents  
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:  
Assistant Commissioner for Patents  
Washington D.C. 20231, on  
November 22, 1999  
Date of Deposit  
William H. Wright/BN 36,312  
Name  
[Signature] 11/22/99  
Signature Date

Dear Sir:

This information disclosure statement is filed to place of record the patent listed on the attached form. No fee is due for filing this statement because it is filed within three months of filing this application and because it is filed before action on the merits of this application.

Respectfully submitted,

LOEB & LOEB LLP

Date: 22 November 1999

By: [Signature]  
WILLIAM H. WRIGHT  
Registration No. 36,312  
Attorney for Applicants

10100 Santa Monica Blvd., 22nd Floor  
Los Angeles, California 90067-4164  
Telephone: 310-282-2260  
Facsimile: 310-282-2192

RECEIVED  
NOV 23 1999  
TELECOMMUNICATIONS ROOM